

Universitatea Națională de Știință și Tehnologie Politehnica București Facultatea de Electronică, Telecomunicații și Tehnologia Informației



## **COURSE DESCRIPTION**

## 1. Program identification information

1.1 Higher education institution	National University of Science and Technology Politehnica Buchares	
1.2 Faculty	Electronics, Telecommunications and Information Technology	
1.3 Department	Electronic Devices, Circuits and Architectures	
1.4 Domain of studies	Electronic Engineering, Telecommunications and Information Technology	
1.5 Cycle of studies	Masters	
1.6 Programme of studies	Micro and Nanoelectronics	

## 2. Date despre disciplină

2.1 Course name (ro) (en)				Dispozitive nanoelectronice				
2.2 Course Lecturer				Prof. Dr. Dragos Dobrescu				
2.3 Instructor for practical activities			Prof. Dr. Dragos Dobrescu					
2.4 Year of studies	2	2.5 Semester	Ι	2.6. Evaluation type	E	2.7 Course regime	Ob	
2.8 Course type		DS	2.9 Course code	LUPB.04.WI3.U.05-33		2.10 Tipul de notare	Nota	

### **3. Total estimated time** (hours per semester for academic activities)

bi iouis per					
3.1 Number of hours per week	3	Out of which: 3.2 course	2.00	3.3 seminary/laboratory	1
3.4 Total hours in the curricula	42.00	Out of which: 3.5 course	28	3.6 seminary/laboratory	14
Distribution of time:					
Study according to the manual, course support, bibliography and hand notes Supplemental documentation (library, electronic access resources, in the field, etc) Preparation for practical activities, homework, essays, portfolios, etc.					30
Tutoring					
Examinations					28
Other activities (if any): 0					0
3.7 Total hours of individual	58.00				

study	58.00	
3.8 Total hours per semester	100	
3.9 Number of ECTS credit points	4	

### **4. Prerequisites (if applicable)** (where applicable)

4.1 Curriculum	Fundamental Courses of Electronic Devices.



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4.2 Results of	General knowledge of physics, electronic devices and software simulation of
learning	electronic circuits

### 5. Necessary conditions for the optimal development of teaching activities (where applicable)

5.1 Course	The course will take place in a room equipped with a video projector or on the MSTeams platform
5.2 Seminary/ Laboratory/Project	The project will take place in a specific room, which must include: computers, Internet connection, SPICE electronic circuit simulator or MSTeams platform, etc, students with computers with a SPICE simulator installed.

**6. General objective** (*Reffering to the teachers' intentions for students and to what the students will be thought during the course. It offers an idea on the position of course in the scientific domain, as well as the role it has for the study programme. The course topics, the justification of including the course in the currcula of the study programme, etc. will be described in a general manner)* 

For the course: The course is aimed specifically at graduates with a bachelor's degree in the field of Electronic Engineering, Telecommunications and Information Engineering

The discipline presents the fundamental concepts of the field of nanoelectronics and presents the main classes of nanoelectronic devices

For applications: Checking the theories acquired at the course by using the main design data of a nanodispositive in a project developed throughout the semester.

**7. Competences** (*Proven capacity to use knowledge, aptitudes and personal, social and/or methodological abilities in work or study situations and for personal and proffesional growth. They refflect the empolyers requirements.*)

requirements.)	
Specific Competences	<ul> <li>Knowledge of how micro and nanoelectronic devices work;</li> <li>Design of circuits with MOS nanometric transistors, low working voltages and low power consumption;</li> <li>Effective correlation between function and structure in defining, designing and performing programmable (complete) SoCs;</li> <li>Knowledge of current technological aspects and understanding their effect on the realization of analog integrated circuits that are part of analog-digital mixed CMOS VLSI systems;</li> <li>Knowledge of advanced technological processes for micro and nano-scale electronic devices;</li> <li>Use of software tools for advanced simulation of both devices and technological processes;</li> </ul>
Transversal (General) Competences	<ul> <li>Developing the experimental skills necessary for the technological realization, modeling and characterization of new micro and nanoelectronic devices and circuits;</li> <li>Responsible execution of multidisciplinary teamwork tasks, assuming roles on different hierarchical levels;</li> <li>Identifying the need for continuous training and efficient use of information sources and communication resources and assisted professional training (internet portals, specialized software applications, databases, online courses, etc, etc.) both in Romanian and in a language of international circulation.</li> </ul>



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**8. Learning outcomes** (Synthetic descriptions for what a student will be capable of doing or showing at the completion of a course. The learning outcomes reflect the student's acomplishments and to a lesser extent the teachers' intentions. The learning outcomes inform the students of what is expected from them with respect to performance and to obtain the desired grades and ECTS points. They are defined in concise terms, using verbs similar to the examples below and indicate what will be required for evaluation. The learning outcomes will be formulated so that the correlation with the competences defined in section 7 is highlighted.)

highlig	ntea.)
Knowledge	The result of knowledge aquisition through learning. The knowledge represents the totality of facts, priciples, theories and practices for a given work or study field. They can be theoretical and/or factual. List the fundamental models of MOS transistors Define model parameters Describe/classify model parameters Highlights the peculiarities of special constructive solutions
Skills	The capacity to apply the knowledge and use the know-how for completing tasks and solving problems. The skills are described as being cognitive (requiring the use of logical, intuitive and creative thinking) or practical (implying manual dexterity and the use of methods, materials, tools and intrumentation). Select and group relevant information about nanoelectronic devices. Arguably uses specific principles of nanoelectronics Work productively in a team to carry out the project. Elaborates a scientific text in drafting the project Solves practical applications within the project It interprets proper causal relationships Analyze and compare nanoelectronic devices. Identifies solutions and elaborates the discipline project. Conclusions on the experiments carried out. Arguments the solutions identified in the project .
Responsability and autonomy	The student's capacity to autonomously and responsably apply their knowledge and skills. Select appropriate bibliographic sources and analyze them. Respect the principles of academic ethics, correctly quoting the used bibliographic sources. Demonstrate responsiveness for new learning contexts. Demonstrates collaboration with other colleagues and teachers in carrying out teaching activities Demonstrates autonomy in organizing the learning situation/context or problem-solving situation Promotes/contributes through new solutions, related to the specialty field. Awareness of the value of its contribution to the field of engineering in identifying viable/sustainable solutions Apply ethical principles

**9. Teaching techniques** (Student centric techniques will be considered. The means for students to participate in defining their own study path, the identification of eventual fallbacks and the remedial measures that will be adopted in those cases will be described.)

Building on the analysis of students' learning characteristics and their specific needs, the teaching process will explore teaching methods both expository (lecture, exposure) and conversational-interactive, based on learning by discovery models facilitated by direct and indirect exploration of reality (experiment, demonstration, modeling), but also on action-based methods such as exercise, exercise, and, practical activities and problem solving.



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Lectures will be used in the teaching activity, based on PowerPoint presentations or different Internet pages that will be made available to students. Each course will start with the recapitulation of the chapters already covered, with an emphasis on the notions taken at the last course.

The presentations use images and schemes so that the information presented is easily understood and assimilated.

This discipline covers information and practical activities designed to support students in their learning efforts and to develop optimal relationships of collaboration and communication in a climate conducive to learning through discovery.

The practice of active listening and assertive communication skills, as well as feedback building mechanisms, will be considered, as ways of behavioral regulation in various situations and of adapting the pedagogical approach to the learning needs of the students.

The ability to work in teams to solve different learning tasks will be practiced.

The attention of students will be checked by rapid tests (quizz) during or at the end of the course at certain courses.

#### **10.** Contents

COURSE					
Chapter	Content	No. hours			
1	Introduction. Theme of the course of nanoelectronic devices. Overview of specific objectives. Presentation of the project theme to the discipline of nanoelectronic devices.	2			
2	General concepts of nanodispositive physics. Particles, crystals, waves in crystals.	2			
3	Growth of mono and hetero crystalline structures. Nanostructuror manufacturing and measurement techniques.	4			
4	Transport mechanisms of load carriers in semiconductors and nanostructures.	2			
5	The behavior of electrons in nanometre-sized structures (pits, wires and quantum dots).	4			
6	Nanoelectronic devices: resonant tunnel diodes, field effect nanotransistors, single electron transfer devices (SET), graphene and graphene-based devices. Automated cells based on quantum dots.	10			
7	Applications of nanoelectronic devices in various fields: engineering, medicine, environment, energy.	4			
	Total:	28			



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### **Bibliography:**

Dobrescu, DN https://curs.upb.ro/2023/enrol/index.php?id=9680

A. Rusu, "Modelarea Componentelor Microelectronice Active", Editura Academiei Romane, 1990.

A.Rusu, "Conductie electrica neliniara in structuri semiconductoare", Editura Academiei Romane, Bucuresti, 2000;

L. Dobrescu, D. Dobrescu, "Modele avansate ale dispozitivelor MOS", Editura Printech, Bucuresti, 2002;

L. Dobrescu, D.Dobrescu, "Basics of the Semiconductor Devices Physics", 142 pg., Ed. Printech, ISBN 973-718-364-9, Bucuresti, 2005.

V. Miltin, V. Kochealp, M.Stroscio, Introduction to Nanoelectronics, Cambridge Press, 347 pg., 2008

PROJECT				
Crt. no.	Content			
1	Choice of teams consisting of 2-4 students and allocation of differentiated project themes	2		
2	Research activity, realization and final presentation of differentiated project themes containing nanoelectronic devices, realization technologies, specific applications of a device, electronic circuit or an integrated microsystem. The theme may also be an extension of the diploma project or a portion of scientific concerns for the realization of the dissertation project.	10		
3	Verification of projects	2		
	<b>Total:</b> 14			
D. Do A. Ru	<b>Bibliography:</b> D. Dobrescu https://curs.upb.ro/2023/enrol/index.php?id=9680 A. Rusu, "Modelarea Componentelor Microelectronice Active", Editura Academiei Romane, 1990. A.Rusu, "Conductie electrica neliniara in structuri semiconductoare",Editura Academiei Romane,			

Bucuresti, 2000;

L. Dobrescu, D. Dobrescu, "Modele avansate ale dispozitivelor MOS", Editura Printech, Bucuresti, 2002;

### 11. Evaluation

Activity type	11.1 Evaluation criteria	11.2 Evaluation	11.3 Percentage of
		methods	final grade



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	knowledge of fundamental theoretical concepts for the realization and operation of nanoelectronic devices	written examination	30%	
11.4 Course	Solving a home theme that presents a device/app/technology corresponding to the course	Colocviu 8 / 2000 ă â î ş ţ ţ colloquium	20%	
11.5 Seminary/laboratory/project	Analyzing the quality of the scientific content of the project and the accuracy of the calculations.			
11.6 Passing conditions				
Highlighting the main classes of nanoelectronic devices.				
Obtaining 50% of the project score during the semester.				

Compliance with the UNSTPB regulation on promotion conditions.

12. Corroborate the content of the course with the expectations of representatives of employers and representative professional associations in the field of the program, as well as with the current state of knowledge in the scientific field approached and practices in higher education institutions in the European Higher Education Area (EHEA)

The increasing complexity of electronic circuits and systems and the need to reduce costs and researchdesign-manufacture cycles have imposed the development of computer-aided simulation, design and optimization techniques, in the form of various software tools.

The discipline provides graduates with adequate skills with the needs of current qualifications and modern, quality and competitive scientific and technical training.

Thus, the graduates are provided with a modern, quality and competitive scientific and technical training that will allow them to be hired quickly after graduation, being perfectly framed in the politics of the Polytechnic University of Bucharest, both in terms of content and structure, and in terms of international skills and openness offered to students.

Date	Course lecturer	Instructor(s) for practical activities
09.09.2024	Prof. Dr. Dragos Dobrescu	Prof. Dr. Dragos Dobrescu



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Date of department approval

Head of department

31.10.2024

Prof. Dr. Claudius DAN

Date of approval in the Faculty Council Dean

01.11.2024

Prof. Dr. Mihnea Udrea

