



**Universitatea Națională de Știință și Tehnologie Politehnica București**  
**Facultatea de Electronică, Telecomunicații și**  
**Tehnologia Informației**



**COURSE DESCRIPTION**

**1. Program identification information**

1.1 Higher education institution	National University of Science and Technology Politehnica Bucharest
1.2 Faculty	Electronics, Telecommunications and Information Technology
1.3 Department	Electronic Devices, Circuits and Architectures
1.4 Domain of studies	Electronic Engineering, Telecommunications and Information Technology
1.5 Cycle of studies	Masters
1.6 Programme of studies	Micro and Nanoelectronics

**2. Date despre disciplină**

2.1 Course name (ro) (en)				Funcțiile dispozitivelor semiconductoare Functions of semiconductor devices			
2.2 Course Lecturer				S.l./Lect. Dr. Miron Cristea			
2.3 Instructor for practical activities				S.l./Lect. Dr. Miron Cristea			
2.4 Year of studies	1	2.5 Semester	II	2.6. Evaluation type	E	2.7 Course regime	Ob
2.8 Course type		DA	2.9 Course code	UPB.04.M1.O.05-10		2.10 Tipul de notare	Nota

**3. Total estimated time (hours per semester for academic activities)**

3.1 Number of hours per week	3	Out of which: 3.2 course	2.00	3.3 seminary/laboratory	1
3.4 Total hours in the curricula	42.00	Out of which: 3.5 course	28	3.6 seminary/laboratory	14
Distribution of time:					hours
Study according to the manual, course support, bibliography and hand notes Supplemental documentation (library, electronic access resources, in the field, etc) Preparation for practical activities, homework, essays, portfolios, etc.					55
Tutoring					0
Examinations					28
Other activities (if any):					0
3.7 Total hours of individual study	83.00				
3.8 Total hours per semester	125				
3.9 Number of ECTS credit points	5				

**4. Prerequisites (if applicable) (where applicable)**



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4.1 Curriculum	<ul style="list-style-type: none"><li>• Electronic devices</li><li>• Electronic components</li></ul>
4.2 Results of learning	General knowledge of <ul style="list-style-type: none"><li>• Physics</li><li>• Electronic devices</li><li>• Simulation software for Electronics</li></ul>

**5. Necessary conditions for the optimal development of teaching activities** (where applicable)

5.1 Course	<ul style="list-style-type: none"><li>• The course is presented using an overhead projector and a laptop</li></ul>
5.2 Seminary/ Laboratory/Project	Teaching is based on using software tools for semiconductor device design

**6. General objective** (*Referring to the teachers' intentions for students and to what the students will be thought during the course. It offers an idea on the position of course in the scientific domain, as well as the role it has for the study programme. The course topics, the justification of including the course in the curricula of the study programme, etc. will be described in a general manner*)

This discipline is studied within the Electronics field of specialization and aims to familiarize students with the main approaches, models and explanatory theories of the field, used in solving practical applications and problems, with relevance for stimulating the learning process in students.

The discipline addresses the following basic/advanced notions, specific concepts and principles as a specific topic, all of which contribute to the transmission/formation of students with an overview of the methodological and procedural benchmarks related to the field.

**7. Competences** (*Proven capacity to use knowledge, aptitudes and personal, social and/or methodological abilities in work or study situations and for personal and professional growth. They reflect the employers requirements.*)

<b>Specific Competences</b>	C1. Using fundamental elements relating to the circuits, systems, instrumentation, and electronic technology C4. Design, simulation and testing devices, integrated circuits and micro and nano-systems with modern software tools C5. Electronic devices and integrated circuits modeling using latest modern micro and nanotechnologies C6. Design, simulation and testing of devices, circuits, and optoelectronic systems with modern software tools and micro and nanotechnologies.
<b>Transversal (General) Competences</b>	CT3 Adapting to new technologies, professional and personal development through training using printed documentation sources, specialized software, and electronic resources in Romanian and at least one foreign language.



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**8. Learning outcomes** (*Synthetic descriptions for what a student will be capable of doing or showing at the completion of a course. The learning outcomes reflect the student's accomplishments and to a lesser extent the teachers' intentions. The learning outcomes inform the students of what is expected from them with respect to performance and to obtain the desired grades and ECTS points. They are defined in concise terms, using verbs similar to the examples below and indicate what will be required for evaluation. The learning outcomes will be formulated so that the correlation with the competences defined in section 7 is highlighted.*)

<b>Knowledge</b>	<p><i>The result of knowledge acquisition through learning. The knowledge represents the totality of facts, principles, theories and practices for a given work or study field. They can be theoretical and/or factual.</i></p> <ul style="list-style-type: none"><li>• <b>Lists the most important stages that marked the development of the field.</b></li><li>• <b>Defines domain-specific notions.</b></li><li>• <b>Describes/classifies notions/processes/phenomena/structures.</b></li><li>• <b>Highlights consequences and relationships.</b></li></ul>
<b>Skills</b>	<p><i>The capacity to apply the knowledge and use the know-how for completing tasks and solving problems. The skills are described as being cognitive (requiring the use of logical, intuitive and creative thinking) or practical (implying manual dexterity and the use of methods, materials, tools and instrumentation).</i></p> <ul style="list-style-type: none"><li>• <b>Selects and groups relevant information in a given context.</b></li><li>• <b>Reasonably uses specific principles.</b></li><li>• <b>Work productively in a team.</b></li><li>• <b>Elaborate a scientific text.</b></li><li>• <b>Solve practical applications.</b></li><li>• <b>Adequately interpret causal relationships.</b></li><li>• <b>Analyze and compare.</b></li><li>• <b>Identifies solutions and develops solution/project plans.</b></li><li>• <b>Formulates conclusions to the experiments carried out.</b></li><li>• <b>Sustain the identified solutions/solutions</b></li></ul>



Responsability and autonomy	<i>The student's capacity to autonomously and responsibly apply their knowledge and skills.</i>
	<ul style="list-style-type: none"><li>• Select appropriate bibliographic sources and analyze them.</li><li>• Respect the principles of academic ethics, correctly citing the bibliographic sources used.</li><li>• Demonstrates responsiveness to new learning contexts.</li><li>• Demonstrates collaboration with other colleagues and teaching staff in carrying out teaching activities</li><li>• Demonstrates autonomy in organizing the learning situation/context or the problem situation to be solved</li><li>• Demonstrates social responsibility through active involvement in student social life/involvement in academic community events</li><li>• Promotes/contributes through new solutions related to the specialized field to improve the quality of social life.</li><li>• Realizes the value of his contribution in the field of engineering to the identification of viable/sustainable solutions to solve problems in social and economic life (social responsibility).</li><li>• Apply principles of professional ethics/deontology in the analysis of the technological impact of the proposed solutions in the specialized field on the environment.</li><li>• Analyzes and capitalizes on business/entrepreneurial development opportunities in the specialized field.</li><li>• Demonstrates real-life situation management skills (collaborative vs. conflict time management).</li></ul>

**9. Teaching techniques** *(Student centric techniques will be considered. The means for students to participate in defining their own study path, the identification of eventual fallbacks and the remedial measures that will be adopted in those cases will be described.)*

Starting from the analysis of students' learning characteristics and their specific needs, the teaching process will explore both expository (lecture, exposition) and conversational-interactive teaching methods, based on discovery learning models facilitated by direct exploration and indirect of reality (experiment, demonstration, modelling), but also on action-based methods, such as exercise, practical activities and problem solving.

In the teaching activity, lectures will be used, based on Power Point presentations or different videos that will be made available to the students. Each course will start with a recap of the chapters already covered, with an emphasis on the concepts covered in the last course.

Presentations use images and diagrams so that the information presented is easy to understand and assimilate.

This discipline covers information and practical activities designed to support students in their learning efforts and the development of optimal collaborative and communicative relationships in a climate conducive to discovery learning.

It will be considered the practice of active listening and assertive communication skills, as well as feedback construction mechanisms, as ways of regulating behavior in various situations and adapting the pedagogical approach to the students' learning needs.

Teamwork skills will be practiced to solve different learning tasks.

## 10. Contents

### COURSE



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Chapter	Content	No. hours
1	Introduction <ul style="list-style-type: none"><li>• Functional electronics – what is it?</li><li>• Analog computational structure and function</li><li>• Dimension and complexity</li></ul>	2
2	Analogic computation <ul style="list-style-type: none"><li>• The mathematical functions of semiconductor devices</li><li>• The analog machine, Computing Analog Blocks (CABs)</li><li>• Programming with electric field, FPAA structure</li><li>• Trans-linear circuits</li></ul>	2
3	The abstract model of analog computing machine <ul style="list-style-type: none"><li>• The analog-computational synergic hierarchy</li><li>• The family of computational semiconductor devices</li></ul>	2
4	Non-linear algebra on the family of semiconductor devices <ul style="list-style-type: none"><li>• Limited IO computability</li><li>• Signal conditioning</li></ul>	2
5	Computation with mathematical functions given by MOS transistors. <ul style="list-style-type: none"><li>• The mathematical functions of MOS general physical models – the linear function, the quadratic function</li><li>• The mathematical functions used in modelling – arctan, square root, tanh</li><li>• The mathematical functions of the static models of subthreshold conduction - exponentials</li></ul>	6
6	Computation with mathematical functions given by types of MOS transistors <ul style="list-style-type: none"><li>• The mathematical functions of multi-gate MOS transistors</li><li>• The mathematical functions of SOI - MOS transistors (Silicon On Insulator) – linear</li><li>• The mathematical functions of other MOS transistors: Trench-gate, SiC</li></ul>	6
7	Computation with mathematical functions of bipolar transistors <ul style="list-style-type: none"><li>• The mathematical functions from general models of bipolar junction transistor - exponential</li><li>• Function particularities at low/high injection levels</li><li>• Function particularities of SiC bipolar transistor</li></ul>	4
8	Computation with mathematical functions of semiconductor diodes <ul style="list-style-type: none"><li>• Mathematical functions of p-n diodes - exponential</li><li>• Mathematical functions of Schottky diode - exponential</li><li>• Mathematical functions of gated diode - linear</li><li>• Functions derived from parasitic elements - linear</li></ul>	4
	<b>Total:</b>	28



### Bibliography:

Miron Cristea, Functions of Semiconductor Devices. Ed. Matrix Rom, 2024

Online materials <https://archive.curs.upb.ro/2022/course/view.php?id=10430>

Rusu, A., "Non-linear Electrical Conduction in Semiconductor Structures", Romanian Academy Publishing House, 2000

L. Dobrescu, D. Dobrescu, „Advanced Models for MOS Devices”, Printech Publishing House, Bucharest, 2002

L. Dobrescu, D. Dobrescu, "Basics of the Semiconductor Devices Physics", 142 pg., Printech Publishing House, ISBN 973-718-364-9, Bucharest, 2005

M. Cristea, "Power Semiconductor Devices", PoliPress, 2016

[http://helpme.scudc.scu.edu/hspice2001/hspice\\_and\\_qrg/hspice\\_2001\\_2-168.html](http://helpme.scudc.scu.edu/hspice2001/hspice_and_qrg/hspice_2001_2-168.html)

<http://www.radio-electronics.com/info/data/semicond/fet-field-effect-transistor/trenchmos-gatetransistors.php>

[http://www.ele.uri.edu/courses/ele448/HspiceRef/hspice\\_mosmod.pdf](http://www.ele.uri.edu/courses/ele448/HspiceRef/hspice_mosmod.pdf)

### LABORATORY

Crt. no.	Content	No. hours
1	Design and modeling of a MOS transistor with functional optimization.	4
2	Design and modeling of a bipolar transistor with functional optimization.	4
3	Design and modeling of a semiconductor diode with functional optimization	4
4	Final test	2
Total:		14

### Bibliography:

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Online materials <https://archive.curs.upb.ro/2022/course/view.php?id=10430>

M. Cristea, "Power Semiconductor Devices", PoliPress, 2016

A. Rusu, "Non-linear Electrical Conduction in Semiconductor Structures", Romanian Academy Publishing House, 2000

### 11. Evaluation

Activity type	11.1 Evaluation criteria	11.2 Evaluation methods	11.3 Percentage of final grade
11.4 Course	- fundamental functioning principles; - basic knowledge for advanced modeling and simulation of electronic devices	Presentation of scientific results from research and outstanding verification test exam at the end of the semester held at a date fixed during the exam session	40%



11.5 Seminary/laboratory/project	- applying specific design and simulation techniques using simulators. - understanding the influence of model parameters and parasitic elements on device functions.	The assessment is cumulated for two intermediate moments: design verification and final verification. It includes: 1. Correctness of activity results 2. A parameter model must be detailed and its influence on the device functions must be explained	60%
11.6 Passing conditions			
Obtaining 50% of the total score. Obtaining 50% of the score related to the activity during the semester.			

**12. Corroborate the content of the course with the expectations of representatives of employers and representative professional associations in the field of the program, as well as with the current state of knowledge in the scientific field approached and practices in higher education institutions in the European Higher Education Area (EHEA)**

- The increasing complexity of electronic circuits and systems and the need to reduce costs and – manufacturing-research-design cycle required the development of simulation techniques, computer aided design and optimization in the form of various software tools.
- The course provides graduates' appropriate skills for the current qualifications needs and a competitive and modern scientific and technical knowledge.
- Providing a scientific and technical training for graduates to obtain a rapid employment after graduation, being perfectly integrated in National University of Science and Technology POLITEHNICA Bucharest policy, both in terms of content and structure, and from the point of view of skills and international openness offered to students.

Date Course lecturer Instructor(s) for practical activities

22.10.2024 S.I./Lect. Dr. Miron Cristea S.I./Lect. Dr. Miron Cristea

*[Handwritten signatures]*

Date of department approval Head of department

31.10.2024 Prof. Dr. Claudiu DAN

*[Handwritten signature]*

Date of approval in the  
Faculty Council

Dean



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01.11.2024

Prof. Dr. Mihnea Udrea